

Download Accelerator Programming Using Directives

Sandra Wienke, RWTH Aachen University, Aachen Germany Sridutt Bhalachandra, ... , USA. About the Workshop. Current hardware trends lead to ever more complex compute node architectures offering multiple, heterogeneous levels of massive parallelism. Applications using the Intel ® FPGA SDK for OpenCL™ have two main components: the FPGA programming bitstream(s), and the host program that manages the application and FPGA accelerator. Parallel computing is a type of computation in which many calculations or the execution of processes are carried out simultaneously. Large problems can often be divided into smaller ones, which can then be solved at the same time. 1. Introduction 1.1. Document Scope and Assumptions. This document provides an overview and introduction to the use of the Cray XC40/50 (Onyx) located at the ERDC DSRC, along with a description of the specific computing environment on Onyx., Accelerator Programming Using Directives.

Other Files :

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